

Fig. 1

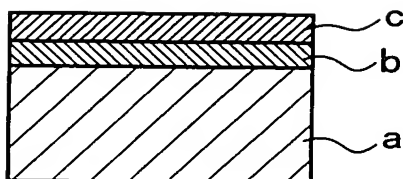


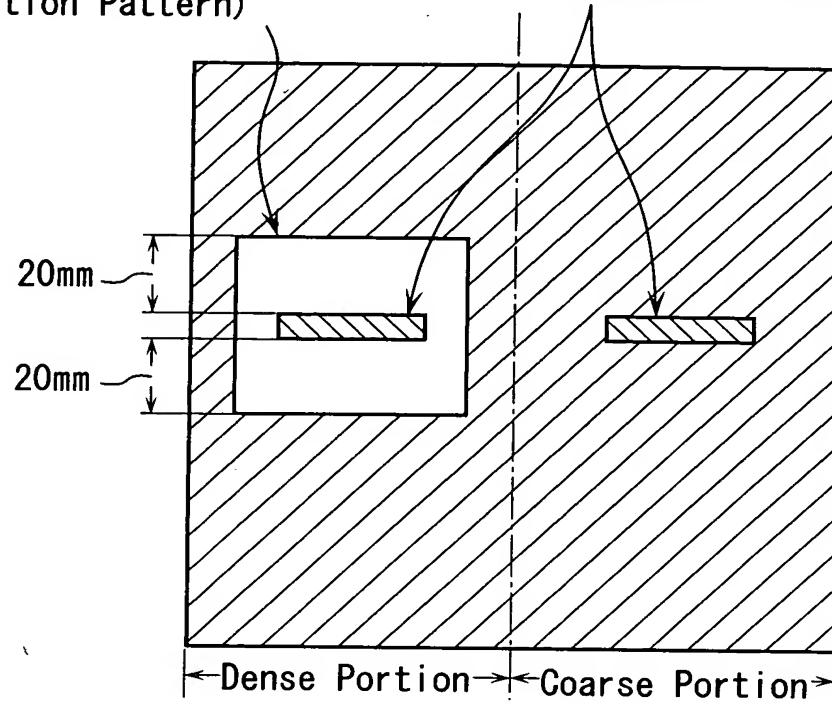
Fig. 2

Flow	Electron Beam Patterning Process	Laser Beam Patterning Process
Receipt of Blank		
Exposure/Patterning	Electron Beam Patterning Device	Laser Beam Patterning Device
Development	Spray, Dip, Paddle Systems	
	Organic Solvent Development Alkali Development	Alkali Development
Post-Baking	Hot Plate Oven Convection Oven	Generally, any treatment is not required.
De-scumming	Plasma De-scumming Device	Generally, any treatment is not required.
CrEtching	Wet Etching and Dry-Etching	
Removal of Resist	Solvent Peeling, Ashing	Exposure of Whole Surface/Alkali Peeling, Solvent Peeling, Ashing
Washing	Sheet-Fed Acid-Treatment, Physical Scrubbing, or the like	
To Inspection Step		

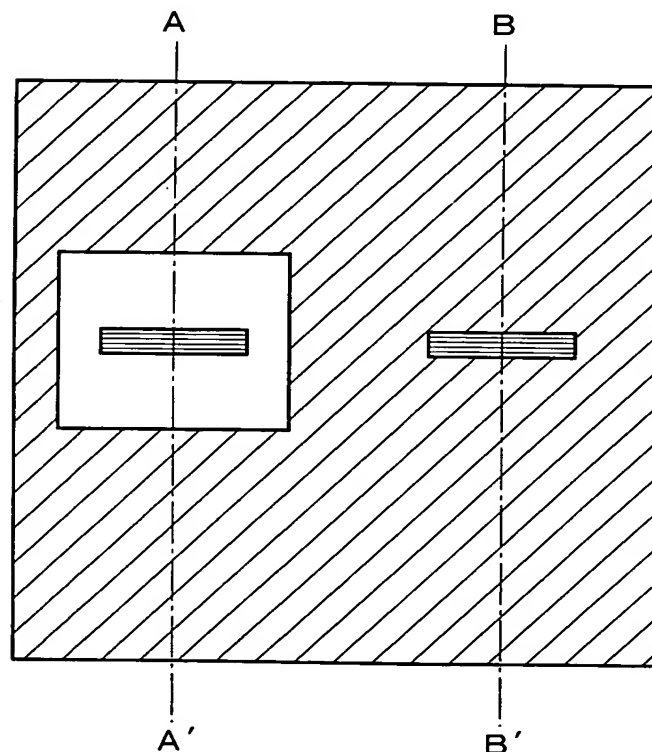
Fing. 5 A

Completely Removed Pattern
 (Periphery of the Dimension-
 Evaluation Pattern)

Dimension-Evaluation Pattern
 (L/S, Isolated L, Isolated S)



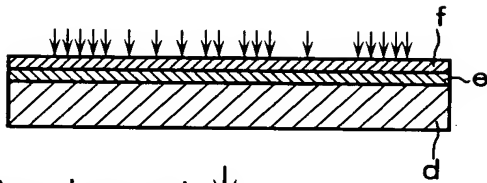
Fing. 5 B



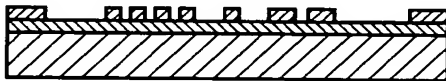
Fing. 6 A

Test Pattern on Dense Portion (A-A')

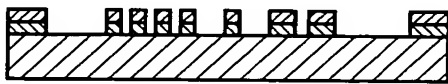
(a) EB Patterning



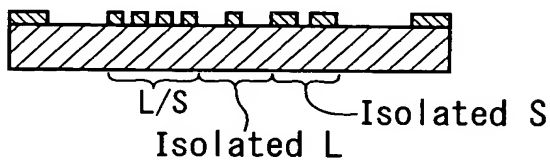
(b) Development ↓



(c) Etching ↓



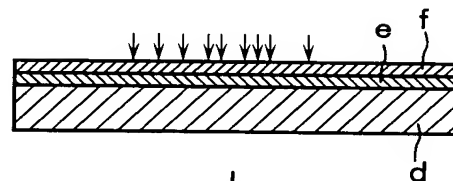
(d) Removal of Resist ↓



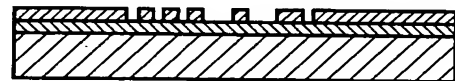
Fing. 6 B

Test Pattern on Coarse Portion (B-B')

(a) EB Patterning



(b) Development ↓



(c) Etching ↓



(d) Removal of Resist ↓

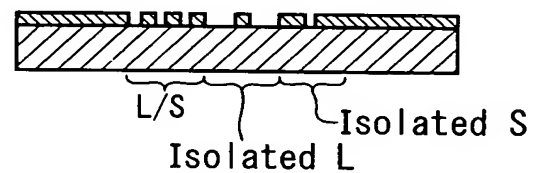
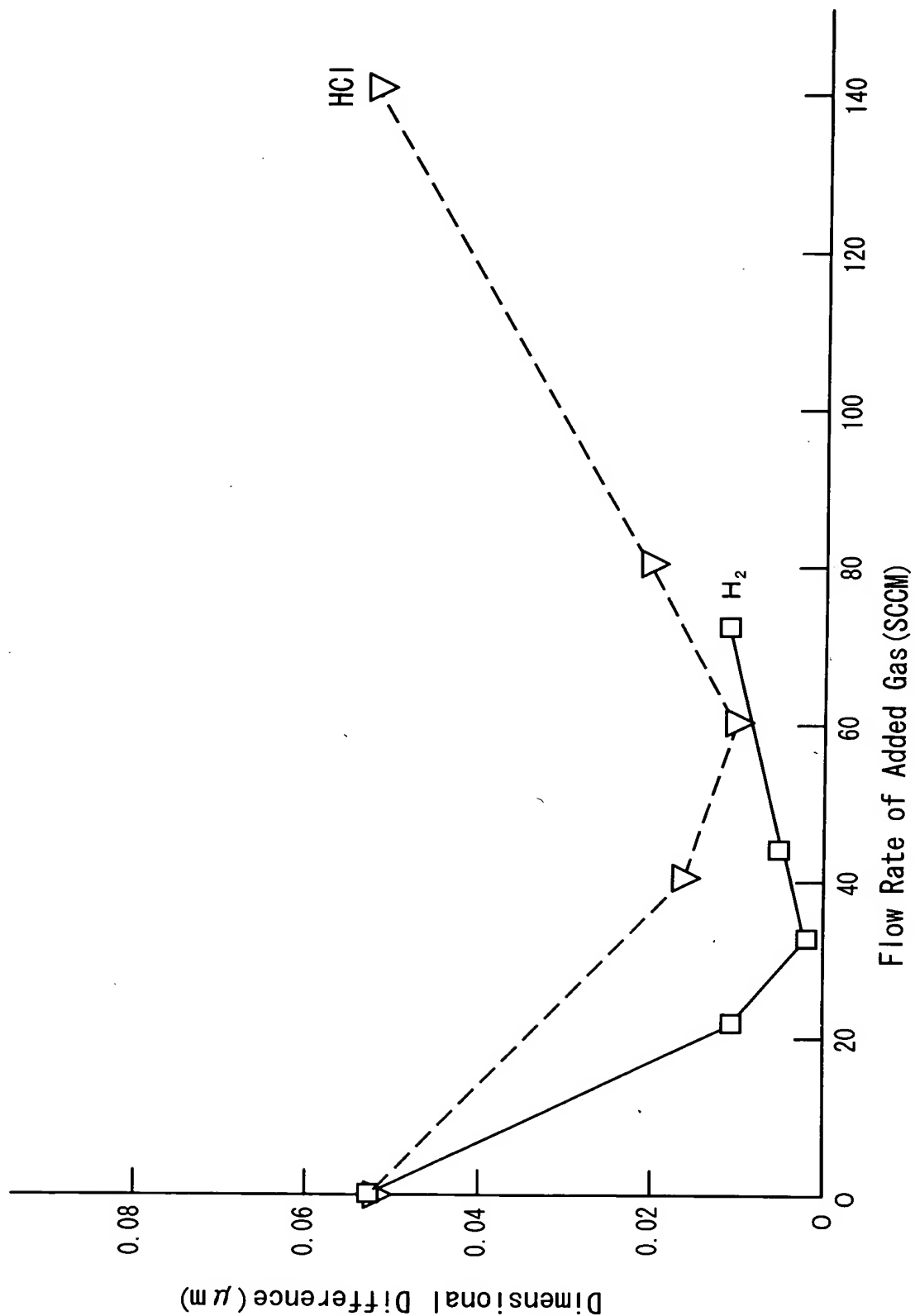


Fig. 7



The diagram shows a rectangular area divided into two main sections. The top section is labeled "Memory Circuit Portion" and contains four horizontal rectangles, each filled with vertical lines. The bottom section is labeled "Peripheral Circuit Portion" and contains four horizontal rectangles, each filled with vertical lines. The labels are connected to their respective sections by curved lines.

The diagram illustrates a computer system architecture divided into two main functional areas:

- Memory Circuit Portion:** This section, located on the left, contains eight rectangular blocks arranged in a 4x2 grid. Each block is filled with vertical lines, representing memory modules or storage units.
- Logic Circuit Portion:** This section, located on the right, is a large rectangular area filled with diagonal lines, representing the logic circuitry or processing units.

The two portions are separated by a vertical line, indicating their distinct roles in the system's operation.